

P-Channel Enhancement Mode Power MOSFET

Description

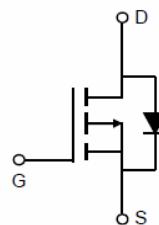
The HM85P02 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

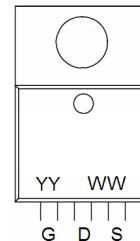
- $V_{DS} = -20V, I_D = -85A$
- $R_{DS(ON)} < 7m\Omega @ V_{GS} = -4.5V$
- $R_{DS(ON)} < 9m\Omega @ V_{GS} = -2.5V$
- $R_{DS(ON)} < 12m\Omega @ V_{GS} = -1.8V$
- High density cell design for ultra low $R_{DS(on)}$
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- Load switch
- Battery protection



Schematic diagram



Marking and pin Assignment



TO-220 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM85P02	HM85P02	TO-220-3L	-	-	-

Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 12	V
Drain Current-Continuous	I_D	-85	A
Drain Current-Continuous($T_C=100^\circ C$)	$I_D (100^\circ C)$	-60	A
Pulsed Drain Current	I_{DM}	-255	A
Maximum Power Dissipation	P_D	80	W
Derating factor		0.64	W/ $^\circ C$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	$R_{\theta JC}$	1.6	$^\circ C/W$
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Electrical Characteristics ($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=-250\mu\text{A}$	-20	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=-16\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 12\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 100	nA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{th})}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=-250\mu\text{A}$	-0.4	-0.6	-1.0	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=-4.5\text{V}, \text{I}_D=-20\text{A}$	-	5.8	7	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-2.5\text{V}, \text{I}_D=-20\text{A}$	-	7.2	9	
		$\text{V}_{\text{GS}}=-1.8\text{V}, \text{I}_D=-20\text{A}$	-	9	12	
Forward Transconductance	g_{FS}	$\text{V}_{\text{DS}}=-5\text{V}, \text{I}_D=-20\text{A}$	80	-	-	S
Dynamic Characteristics ^(Note 4)						
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=-10\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1.0\text{MHz}$	-	3500	-	PF
Output Capacitance	C_{oss}		-	577	-	PF
Reverse Transfer Capacitance	C_{rss}		-	445	-	PF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$\text{V}_{\text{DD}}=-10\text{V}, \text{R}_{\text{GEN}}=3\Omega$ $\text{V}_{\text{GS}}=-4.5\text{V}, \text{R}_{\text{L}}=0.5\Omega$	-	18	-	nS
Turn-on Rise Time	t_r		-	42	-	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	85	-	nS
Turn-Off Fall Time	t_f		-	23	-	nS
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=-10\text{V}, \text{I}_D=-20\text{A},$ $\text{V}_{\text{GS}}=-4.5\text{V}$	-	55	-	nC
Gate-Source Charge	Q_{gs}		-	10	-	nC
Gate-Drain Charge	Q_{gd}		-	15	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V_{SD}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_s=-20\text{A}$	-	-	-1.2	V
Diode Forward Current ^(Note 2)	I_s		-	-	-85	A
Reverse Recovery Time	t_{rr}	$\text{TJ} = 25^\circ\text{C}, \text{IF} = -10\text{A}$ $d\text{i}/dt = 100\text{A}/\mu\text{s}$ ^(Note 3)	-	47	-	nS
Reverse Recovery Charge	Qrr		-	53	-	nC
Forward Turn-On Time	t_{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Typical Electrical and Thermal Characteristics (Curves)

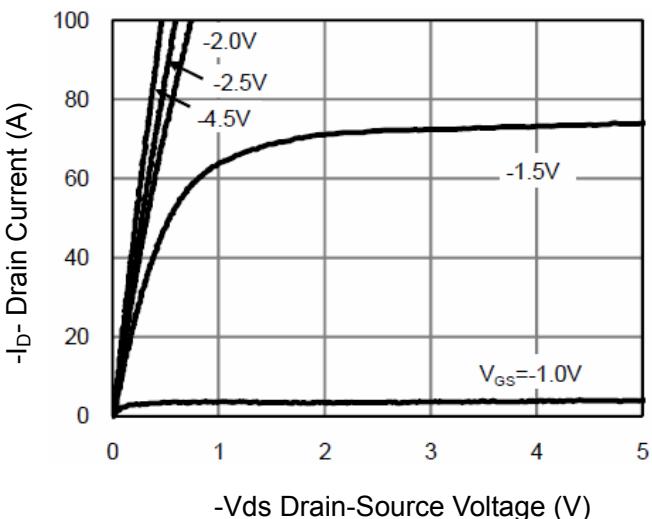


Figure 1 Output Characteristics

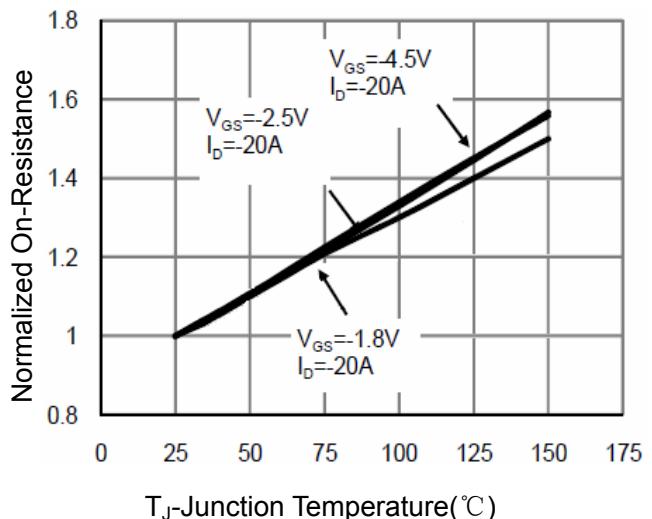


Figure 4 Rdson-Junction Temperature

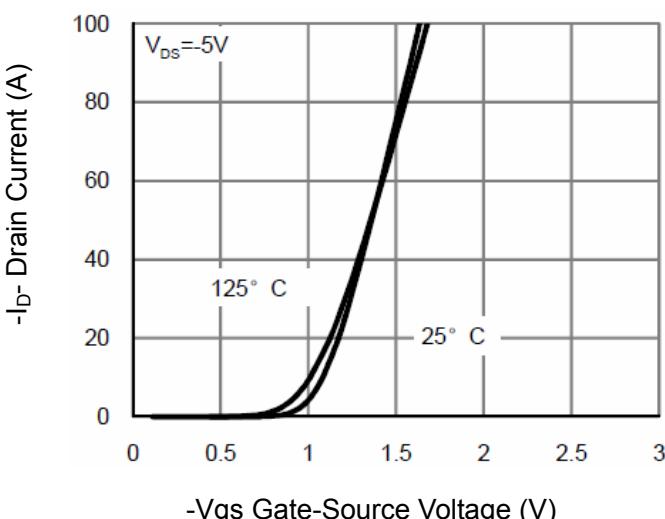


Figure 2 Transfer Characteristics

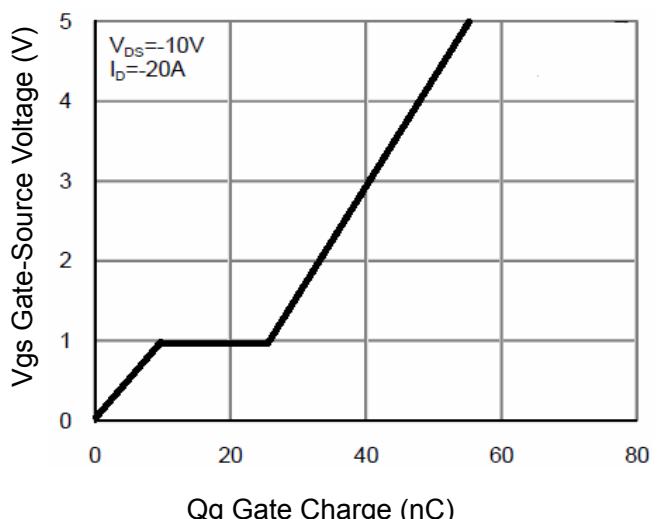


Figure 5 Gate Charge

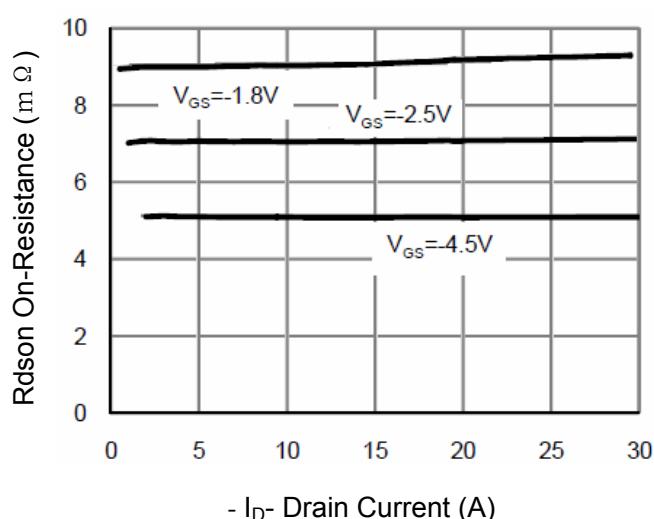


Figure 3 Rdson- Drain Current

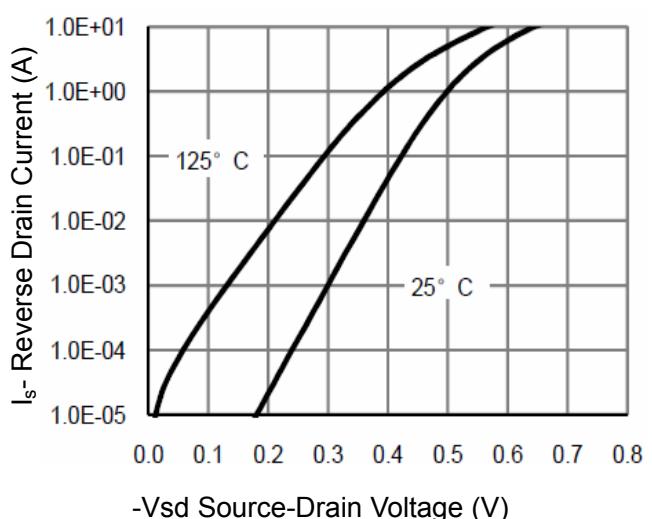


Figure 6 Source- Drain Diode Forward

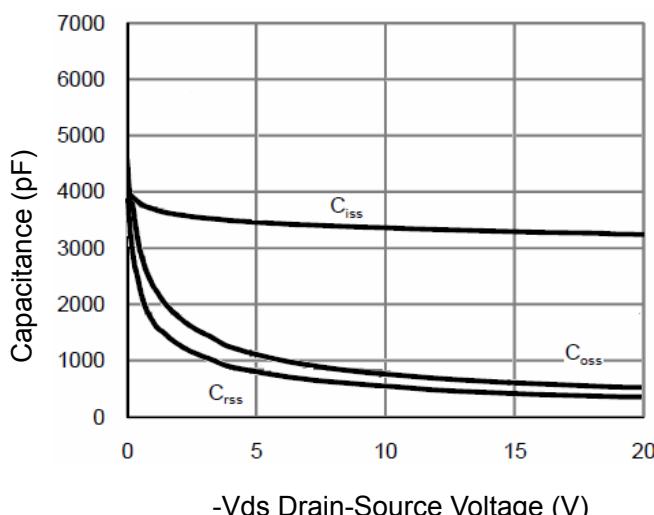


Figure 7 Capacitance vs Vds

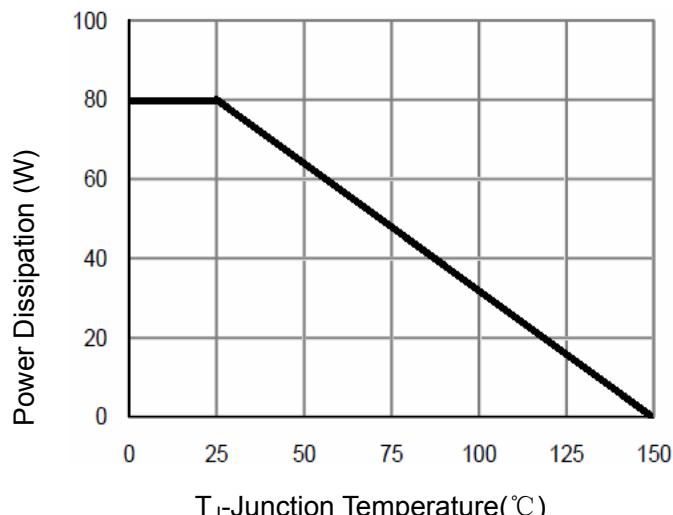


Figure 9 Power De-rating

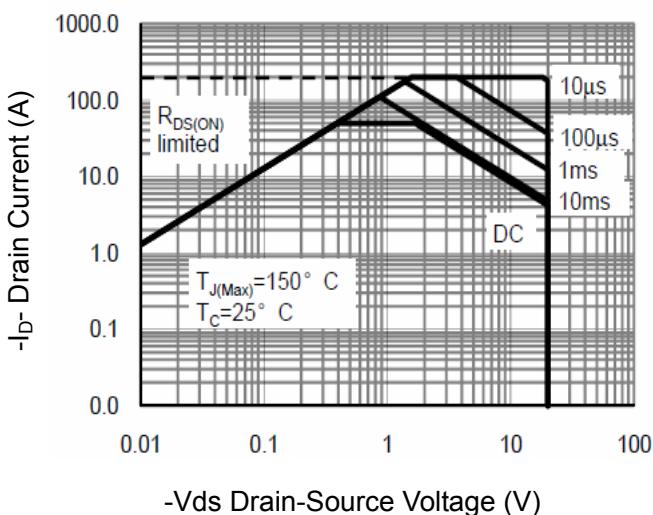


Figure 8 Safe Operation Area

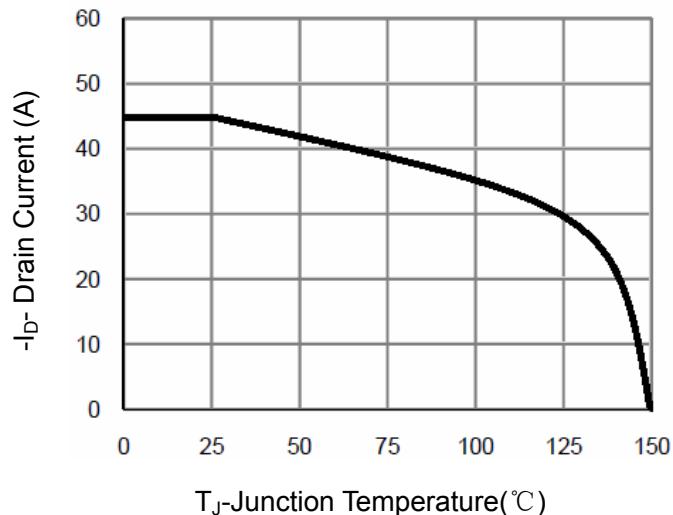


Figure 10 -Current De-rating

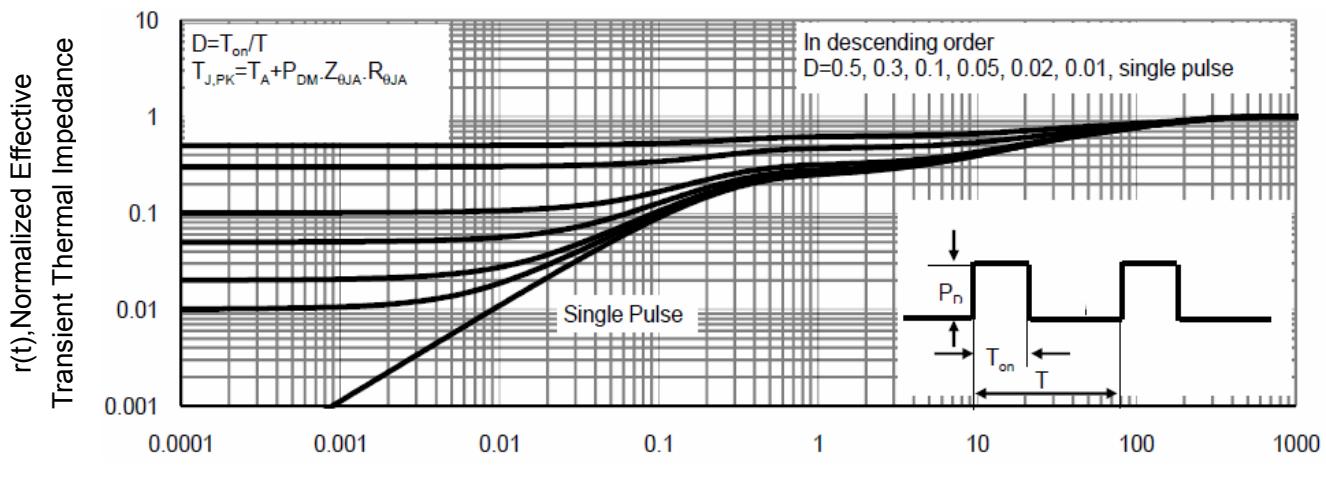
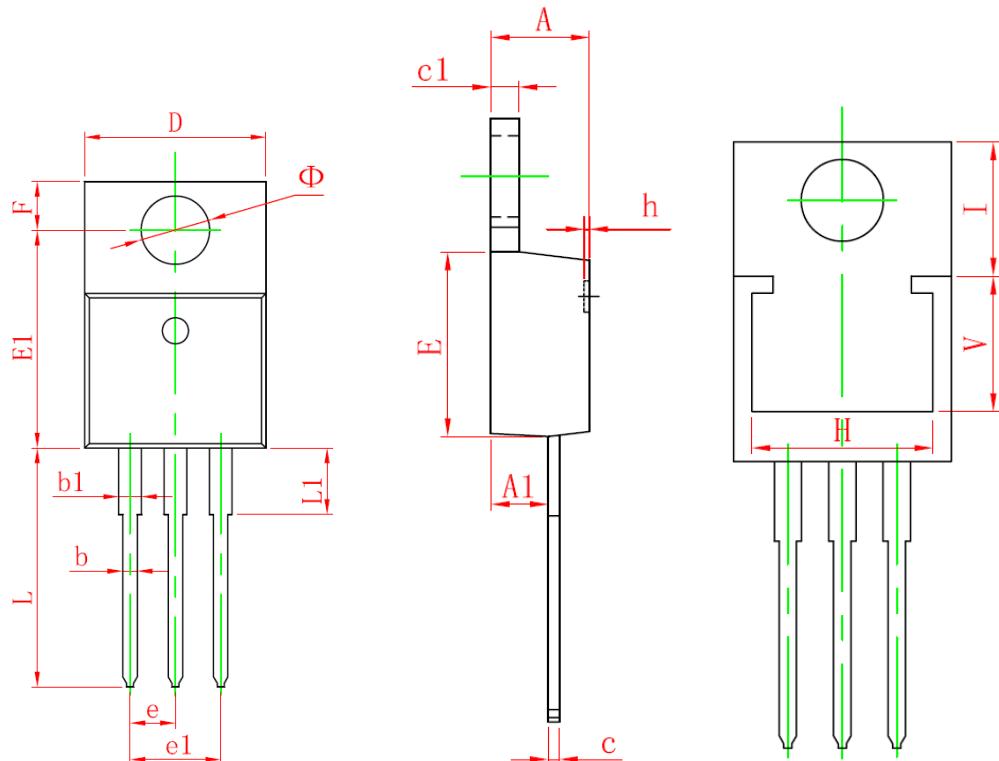


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-220-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	4.470	4.670	0.176	0.184
A1	2.520	2.820	0.099	0.111
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	10.010	10.350	0.394	0.407
E	8.500	8.900	0.335	0.350
E1	12.060	12.460	0.475	0.491
e	2.540 (TYP.)		0.100 (TYP.)	
e1	4.980	5.180	0.196	0.204
F	2.590	2.890	0.102	0.114
H	8.440 REF.		0.332 REF.	
h	0.000	0.300	0.000	0.012
L	13.400	13.800	0.528	0.543
L1	3.560	3.960	0.140	0.156
V	6.360 REF.		0.250 REF.	
I	6.300 REF.		0.248 REF.	
Φ	3.735	3.935	0.147	0.155